




**JSS Mahavidyapeetha**  
**JSS Academy of Technical Education, Bengaluru**  
**Department of Electronics & Communication Engineering**



**FACULTY PROFILE**

**1. Personal Details**

NAME	Dr. Veeramma Yatnalli	
DEPARTMENT	<b>Electronics and Communication Engineering</b>	
DESIGNATION	<b>Associate Professor</b>	
PHONE	<b>9916928507</b>	
EMAIL ID	<b>veerammayatnalli@jssateb.ac.in</b>	
TEACHING EXPERIENCE	<b>24</b>	
INDUSTRY EXPERIENCE	<b>Nil</b>	
RESEARCH EXPERIENCE	<b>10</b>	

**2. Qualification**

COURSES	SPECIALIZATION	INSTITUTION	UNIVERSITY
Ph D	<b>Wireless Communication, and image Processing</b>	<b>Dayananda Sagar college of engineering, Bengaluru</b>	<b>VTU, Belagavi</b>
ATS or any other certification	<b>M.E in Digital Electronics</b>	<b>BVB College of Engineering and Technology, Hubli</b>	<b>Karnatak University, Dharwad</b>
BE / Diploma / ITI	<b>B. E in ECE</b>	<b>SLN College of Engineering, Raichur</b>	<b>Gulbarga University, Gulbarga</b>
PUC	<b>PCMB</b>	<b>LVD College, Raichur</b>	<b>Pre University Board</b>
SSLC	<b>SSLC</b>	<b>Tagore Memorial Girl's High School, Raichur</b>	

**3. Membership of Professional Bodies:**

- Life Membership in Indian Society for Technical Education ( MISTE), Life Member, L M 49032 2006**

**4. Awards**

Award Title	Date of receiving the award	Award issuing authority/ Body / Organization
NIL		

**5. Publications ( Journals Conferences )**

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	WOS / Scopus	Impact Factor	Publisher	Link
01	<b>Wheelchair Control Using Brain Computer Interface</b>	<b>Veeramma Yatnalli , B G Shivaleelavathi, Saroja S Bhusare, Aditi Prakash,</b>	<a href="#"><u>International Journal of Future Generation Communication and Networking</u></a>	<b>WoS</b>	<b>0.15</b>	<b>SERSC Science &amp; Engineering Research Support</b>	<a href="https://sersc.org/journals/index.php/IJFGCN/article/view/35555"><b>https://sersc.org/journals/index.php/IJFGCN/article/view/35555</b></a>

		Nithyashree D	Vol. 14 No. 1 (2021) <a href="#">ISSN: 2233-7857 (Online)</a>			soCiety	
02	Review on Multi Level Inverter Topologies and Control Strategies for Solar Power Conversion	B. G. Shivaleelavathi, Chinmayi, Veeramma Yatnalli	08, 02, 2020, 295~315	WoS	COSMOS: 4.24	Politeknik Elektronika Negeri Surabaya, Indonesia	<a href="https://emitter.pens.ac.id/index.php/emitter/article/view/500">https://emitter.pens.ac.id/index.php/emitter/article/view/500</a>
03	Enhanced Power Added Efficiency for RF Power Amplifiers in Mobile Communication	Ambily Babu, B.G Shivaleelavathi, Veeramma Yatnalli	<a href="#">International Journal of Grid and Distributed Computing</a> Published 2020-12-01 Issue <a href="#">Vol. 13 No. 2 (2020)</a>	WoS	0.15	SERSC Science & Engineering Research Support soCiety	<a href="http://sersec.org/journals/index.php/IJGDC/article/view/34571">http://sersec.org/journals/index.php/IJGDC/article/view/34571</a>
04	Simulation of Seven and Nine Level CHBMLI with Elliptical Phase Disposition PWM Technique	<a href="#">Chinmayi</a> , <a href="#">B. G. Shivaleelavathi</a> , <a href="#">Veeramma Yatnalli</a>	<a href="#">Vol.8 No.3, March 2020</a> Doi: <a href="#">10.4236/jpee.2020.83004</a>		1.13	Journal of Power and Energy Engineering SCIRP	<a href="https://www.scirp.org/journal/paperabs.aspx?paperid=99274">https://www.scirp.org/journal/paperabs.aspx?paperid=99274</a>
05	Review Of Inpainting Algorithms For Wireless Communication Application	Dr. Veeramma Yatnalli, Dr. B. G. Shivaleelavathi, Dr. Sudha K. L	10, 03, 2020, 5790-5795	WoS	0.6	D. G. Pylarinos Greece	<a href="https://www.etasr.com/index.php/ETASR/article/view/3547">https://www.etasr.com/index.php/ETASR/article/view/3547</a>
06	Veeramma Yatnalli Asha P Meghana S Harshitha R Gowda B Bhavana	Implementation of Golay Code in VLSI	Perspectives in Communication, Embedded-Systems and Signal-Processing (PiCES)  2566-932x <i>pices</i> , vol. 4, no. 7, pp. 165-169, Nov. 2020.	--	--	World Serve Online-Germany	<a href="#">Implementation of Golay Code in VLSI   Perspectives in Communication, Embedded-systems and Signal-processing - PiCES (pices-journal.com)</a>
07	Dr.B. G. Shivaleelavathi, Veeramma Yatnalli, Chinmayi	Design and Development of an Intelligent System for Pothole and Hump Identification	International Journal of Recent Technology and Engineering (IJRTE)	Scopus	0.11	Blue Eyes Intelligence Engineering & Sciences Publication (BEIESP) Bhopal	<a href="#">International Journal of Recent Technology and Engineering (IJRTE)</a>

		<b>on Roads</b>	<b>ISSN: 2277-3878, Volume-8 Issue-3, September 2019</b>			<b>(M.P.), India</b>	
<b>08</b>	<b>Joint source-channel coding by Inpainting and RS Code</b>	<b>Dr. Veeramma Yatnalli, Dr. Sudha K. L</b>	Journal of Advanced Research in Dynamical and Control Systems Vol. No.10 Issue No.13 pp: 1663-1678 Year:2018	<b>Scopus</b>	<b>0.11</b>	Institute of Advanced Scientific Research, USA	<a href="https://www.jardcs.org/bac-kissues/abstract.php?archivid=6429">https://www.jardcs.org/bac-kissues/abstract.php?archivid=6429</a>
<b>09</b>	1.Veeramma Yatnalli 2.Dr. K L Sudha	Image Transmission over Multipath Fading Channel and Image Denoising using Directional Weighted Median Filter	International Journal of Computer Applications Vol. No.109 Issue No.10 pp:18-23 Year:2015		0.7		<a href="http://ijcaonline.org">IJCA - Image Transmission over Multipath Fading Channel and Image Denoising using Directional Weighted Median Filter (ijcaonline.org)</a>
<b>10</b>	1.Veeramma Yatnalli 2.Dr. K L Sudha	Image Compression Based on Spatial Redundancy Removal and Reconstruction using Image Inpainting	Int. J. of Recent Trends in Engineering & Technology ACEEE, USA (a division of IDES)  Vol. No.11 Issue No. pp:623-632 Year:2014	--	--		
<b>11</b>	Veeramma Yatnalli  Veeresh Gavai	Design of Pulsed Latch Shift Register	International Research Journal of Engineering and Technology (IRJET) Vol. No.04 Issue No.06 pp:1199-1202 Year:2017	-- Fast Track Publications , India			<a href="https://www.irjet.net/archives/V4/i6/IRJET-V4I6688.pdf">https://www.irjet.net/archives/V4/i6/IRJET-V4I6688.pdf</a>
<b>12</b>	Veeramma Yatnalli	High Speed And Low Power CMOS	International Research Journal of	Fast Track Publica			<a href="https://www.irjet.net/archives/V3/i5/IRJE">https://www.irjet.net/archives/V3/i5/IRJE</a>

	PRIYANKA PAWAR	Technology based Ram-Cam Memory Design	Engineering and Technology (IRJET) Vol. No.05 Issue No.03 pp:623-632 Year:2016	tions , India			T-V315243.pdf
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**a. Publications (Conferences)**

Sl. No	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1	1.Veeramma Yatnalli 2.Dr. K L Sudha	Effect of Image Transmission Over Multipath Fading Channel and Reconstruction Using Inpainting	Emerging Research in Computing, Information, communication and Applications", ERCICA	Elsevier ISBN:9789351072601-02 August-2014, NMIT, Bengaluru Page Nos. :132-139	WoS		
2	Dr. K L Sudha	Patch Based Image Completion for Compression Application  DOI: <a href="https://doi.org/10.1109/ICSIP.2014.20">10.1109/ICSIP.2014.20</a>	Signal and Image Processing (ICSIP), 2014 Fifth International Conference	IEEE INSPEC Accession Number: 14199032 Month & Year 8-10 Jan.2014 Page Nos. : 92 - 95	Scopus		
3	Dr. K L Sudha	Reduced Bit Rate Using Image Inpainting  DOI 10.1007/978-81-322-1157-0	Emerging Research in Electronics, Computer Science and Technology	Springer Print ISBN 978-81-322-1156-3 Volume No-248 Month & Year -2014 Page Nos- 525-535	Scopus		
4	Dr. K L Sudha	Image compression with inpainting  DOI: <a href="https://doi.org/10.1109/ISSPIT.2012.6621279">10.1109/ISSPIT.2012.6621279</a>	Signal Processing and Information Technology (ISSPIT), 2012 IEEE International Symposium	IEEE Print ISBN: 978-1-4673-5604-6 INSPEC Accession Number: 13823701 Month and year- 12-15 Dec.	Scopus		

				2012 Page Nos-158-163			
5	B. G. Shivaleelavathi, Veeramma Yatnalli, Saroja S Bhusare, Madhan Kumar K, Amith T	Smart Health Care and Tracking System Based on Internet of Things. G	National Conference on "Bioengineering-2020"	10 <sup>th</sup> -11 <sup>th</sup> December 2020			Dept. of Biotechnology & Medical Engineering, NIT, Rourkela, Odhisha-769008
6	Veeramma Yatnalli, B. G. Shivaleelavathi, Saroja S Bhusare	Wheelchair Control Using Brain Computer Interface	National Conference on "Bioengineering-2020"	10 <sup>th</sup> -11 <sup>th</sup> December 2020			Dept. of Biotechnology & Medical Engineering, NIT, Rourkela, Odhisha-769008

#### 6. Grants/Funding Received

Grant Amount	Project Name	Date of receiving the Grant	Grant issuing authority/ Body / Organization
	NIL		

#### 6.Others :Workshops / Conference (Orgained/Attended)

##### 6.a. Workshops / Conference organized

Sl. No.	Name of the workshop / Conference	Organised by	Date	Role
1	JSSATEB - NATIONAL SYMPOSIUM – 2018 Internet of Things -Trends & Opportunities	JSSATE, Bengaluru	4th & 5th JULY-2018 JSSATE-Bengaluru	Department Coordinator
2	VGST Sponsored Faculty Development Program On "Internet of Things"	JSSATE, Bengaluru	18 – 23, June 2018 JSSATE-Bengaluru	Department Coordinators
3	Faculty development programme on Embedded Controller Lab using ARM Cortex M3	JSSATE, Bengaluru	4th, 5th & 6th JANUARY, 2018 JSSATE-Bengaluru	Program Coordinator
4	FDP on 5G-Mobile Communication and Edge Computing	JSSATE, Bengaluru	28th July 2017 JSSATE-Bengaluru	Organizer
5	International conference on Signal, Image Processing,	JSSATE, Bengaluru	6th & 7th July 2017	Co-coordinator

	Communication & Automation (ICSIPCA-17)		JSSATE-Bengaluru	& Track In charge
6	Workshop on GNU Radio and Software Defined Radio	JSSATE, Bengaluru	27th June to 1st July 2016 JSSATE-Bengaluru	Organizing committee member
7	Workshop on Signal, Image Processing and Software Defined Radio using Lab View	JSSATE, Bengaluru	22-24, June-2015 JSSATE-Bengaluru	Organizing committee member
8	NATIONAL CONFERENCE ON VLSI, SIGNAL AND IMAGE PROCESSING (NCVSIP-14)	JSSATE, Bengaluru	22 – 23 AUGUST, 2014 JSSATE-Bengaluru	Organizing committee member
9	Fundamentals of Linux and Data Analytics	Vodafone India Foundation and Connecting Dream Foundation, Bangalore	10 <sup>th</sup> August, 2020 to 18 <sup>th</sup> August, 2020	Coordinator
10	AI based Chatbot creation and hosting, NLP with python, Python code contest	Vodafone India Foundation and Connecting Dream Foundation, Bangalore	18 <sup>th</sup> September to 1 <sup>st</sup> October 2020(total 11 Hours)	Coordinator
11	Fundamentals of Linux and Data Analytics	Vodafone India Foundation and Connecting Dream Foundation, Bangalore	August 19, 2020 to August 27, 2020	Coordinator
12	Image Recognition with TensorFlow	Vodafone India Foundation and Connecting Dream Foundation, Bangalore	31 <sup>st</sup> October 2020(2pm to 5pm)	Coordinator

**6.b. Conference Attended ( those sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)**

Sl. No.	Name of the workshop / Conference	Organiser	Date
1	DSP Architecture and Tools	R V College of Engineering Dept of Electronics and Communication Engg. RVCE, Bangalore	from 13th to 15th Sept-2000
2	Train the trainer on Digital signal processor DSP563XX family	BMSCE Dept of Electronics and Communication Engg. BMSCE Bangalore	From 18th to 22nd October 2000
3	Digital Signal Processing and Applications	UVCE Bangalore university, UVCE, Dept of Electronics and Communication Engg. Bangalore	From 29th July to 14th August 2002

4	Advanced electronic Laboratories(VHDL,DSP and Microcontrollers)	RNSIT Dept of Electronics and Communication Engg. Bangalore	14th to 19th February 2005
5	MATLAB, SIMULINK and DSP Processor	Dr. AIT , Dept of Electronics and Communication Engg.Bangalore	From 6th to 8th march 2006
6	A short term course on System C and Applications on VLSI Design	JSSATE, Dept of Electronics and Communication Engg.Bangalore	Dept of Electronics and Communication Engg. JSSATE, Bangalore 6th to 8th April 2005
7	Automatic Speech processing and Understanding	IISc, Bangalore	Dept of Electrical Communication Engg, IISc, Bangalore 4th to 7th January 2006
8	National conference on VLSI, Signal and Image processing,	JSSATE, Dept of Electronics and Communication Engg.Bangalore	Dept of Electronics and Communication Engg. JSSATE, Bangalore 22nd t& 23rd August 2011
9	National conference on Antennas and its applications	VTU, Belguam	VTU, Regional center, Bangalore 24th to 25th February, 2012
10	FPGA Based Embedded System Design	JSSATE, DEXEL EmDAC	Dept of Electronics and Communication Engg. JSSATE, Bangalore On 3rd January, 2012
11	Signal , Image processing and SDR using Lab View	JSSATE, bangalore TECHLABS	Dept of Electronics and Communication Engg. JSSATE, Bangalore 22nd to 24th June, 2015
12	Capacity Building for Women Managers in Higher Education” - SAM Workshop	SJCE,Mysuru	4th to 8th June-2019
13	Engineering Statistics and Linear Algebra	GAT, Bangaluru	2 <sup>nd</sup> – 26 <sup>th</sup> , January 2020
14	Wireless Communication Technologies for IoT	E&ICT Academy IIT Guwahati	27 July to 07 August, 2020
15	Recent Advances in Artificial Intelligence	Computer Science and Engineering Department, National Institute of Technology, Uttarakhand and SLIET Longowal	27th-31st July 2020 sponsored by TEQIP-III
16	Applied Data science	BMSCE, Bangalore	6th -17th July-2020

**6.c. workshop/ Seminar /Conference Attended ( those NOT sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)**

NIL

**6.d. PROJECT / Innovations/Patents**

NIL

**6.e. Any other information you will like to share about your professional experience --**