JSS Mahavidyapeetha
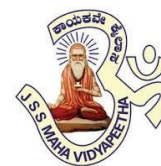# JSS Academy of Technical Education, Bengaluru
Department of Electronics and Communication Engineering

FACULTY PROFILE

1. Personal Details

NAME	Shubha B	
DEPARTMENT	Electronics and Communication Engineering	
DESIGNATION	Assistant Professor	
PHONE	9880293646	
EMAIL ID	shubhab@jssateb.ac.in	
TEACHING EXPERIENCE	22.5 Years	
INDUSTRY EXPERIENCE	-	
RESEARCH EXPERIENCE	7 Years	

2. Qualification

COURSES	SPECIALIZATION	INSTITUTION	UNIVERSITY
Ph.D (Pursuing)	VLSI Design	PES College of Engineering, Mandya	VTU, Belgaum
M.Tech	VLSI Design and Embedded System	Sri Jayachamarajendra College of Engineering, Mysore	VTU, Belgaum
BE	ECE	Sri Jayachamarajendra College of Engineering, Mysore	Mysore
PUC	PCMB	JSS Women's College, Mysore	Karnataka State Pre-University Board
SSLC		JSS Girl's High School, Mysore	Karnataka State Senior Secondary Board

3. Membership of Professional Bodies:

1. Life Membership in Indian Society for Technical Education (MISTE)

4. Awards:

Award Title	Date of receiving the award	Award issuing authority/ Body / Organization
Best Project Award	29-04-2019	4th national Level Project Competition "IEEE Project Expo-2019", GSSS Institute for Engineering and Technology for Women.

4. Publications (Journals Conferences)
   International Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
1	Smart Accident Prevention and Alerting System for Automobiles	Nisha T G, Smitha B R, Suraj Naik, Shubha B	International Journal of Research & Technology	Vol. No.08 Issue No.03 pp:96-101 Year:2020	**No**		
2	Secure Voting System using Aadhar and Biometrics	Sushmita K S, Sanmathi, Supreeth S, Abhishek V S, Shubha B	International Research Journal of Engineering and Technology(IRJET)	Vol. No.06 Issue No.06 pp:875-878 Year:2019	**No**		
3	SCS-MCSA-Based Architecture for Montgomery Modular Multiplication	Dr S T Veerabhadrappa Shubha B Pramoda B S	International Research Journal of Engineering and Technology (IRJET),	Vol. No.04 Issue No.08 pp:379-382 Year:2017	**NO**		
4	BIST Technique for Delay Measurement using Signature Registers	Shivanand Kumbar, Shubha B	International Journal of VLSI System and Communication Systems (IJVDCS)	Vol. No.03 Issue No.03 pp:251-255 Year:2015	**No**		
5	Design of Intelligent ECG device with abnormality detection and wireless Interface	Badrinath B S, Shubha B	International Journal of Advancements in Research & Technology (IJOART),	Year:2014	**No**		

Conferences

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1	Reduction of Power Consumption and Data Transmission on to a Webpage with a Comparative study on Arduino Uno and NodeMCU	Shrinidhi D Sowmya R Sahana Samyuktha Shubha B	Grenze ScientificSociety	pp:390-395 Year:2017	No		
2	Design of a Low voltage Bandgap Reference Circuit	Poornima M, Shubha B	NCDDEE-14, Dept. of Electrical & Electronics Engineering, SSIT Tumkur Proceedings	pp:390-395 Year:2014	No		

5. Grants/Funding Received

Grant Amount	Project Name	Date of receiving the Grant	Grant issuing authority/ Body / Organization
6000/-	NON-INVASIVE GLUCOSE AND CHOLESTEROL MONITORING DEVICE	23rd April 2021	KSCST (44th Series, Student Project)

6.Others :Workshops / Conference (Orgainsed/Attended)

6.a. Workshops / Conference organized

6.b. Conference Attended (those sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)

6.c. workshop/ Seminar /Conference Attended (those NOT sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)

Building Research ECO System, Organised by JSS Academy of Technical Education, Bangalore,02-03-2021 to 04-03-2021
AI and ML Applications in Image Processing using Modern Tools, organized by Ramaiah Institute of Technology, Bangalore, 13-07-2020 to 18-07-2020
Short term course on "Fundamentals of Physical Design & Verification using Cadence Tool Flow", organized by Entuple Technologies Pvt Ltd, 21-01-2019 to 25-01-2019
Electronic System Design, Manufacturing & Testing, organized by BMS College of Engineering, Bangalore, 27-07-2015 to 31-07-2015
Advanced VLSI Design using Cadence Tool Suite, organized by JSS Academy of Technical Education, Bangalore, 16-07-2014 to 18-07-2014

6.d. PROJECT / Innovations/Patents

6.e. Any other information you will like to share about your professional experience

Delivered Lectures on Subject "Analog Electronics" from 20-09-2016 to 27-09-2016 under VTU-EDUSAT programme-21.